

FIG. 1
PRIOR ART

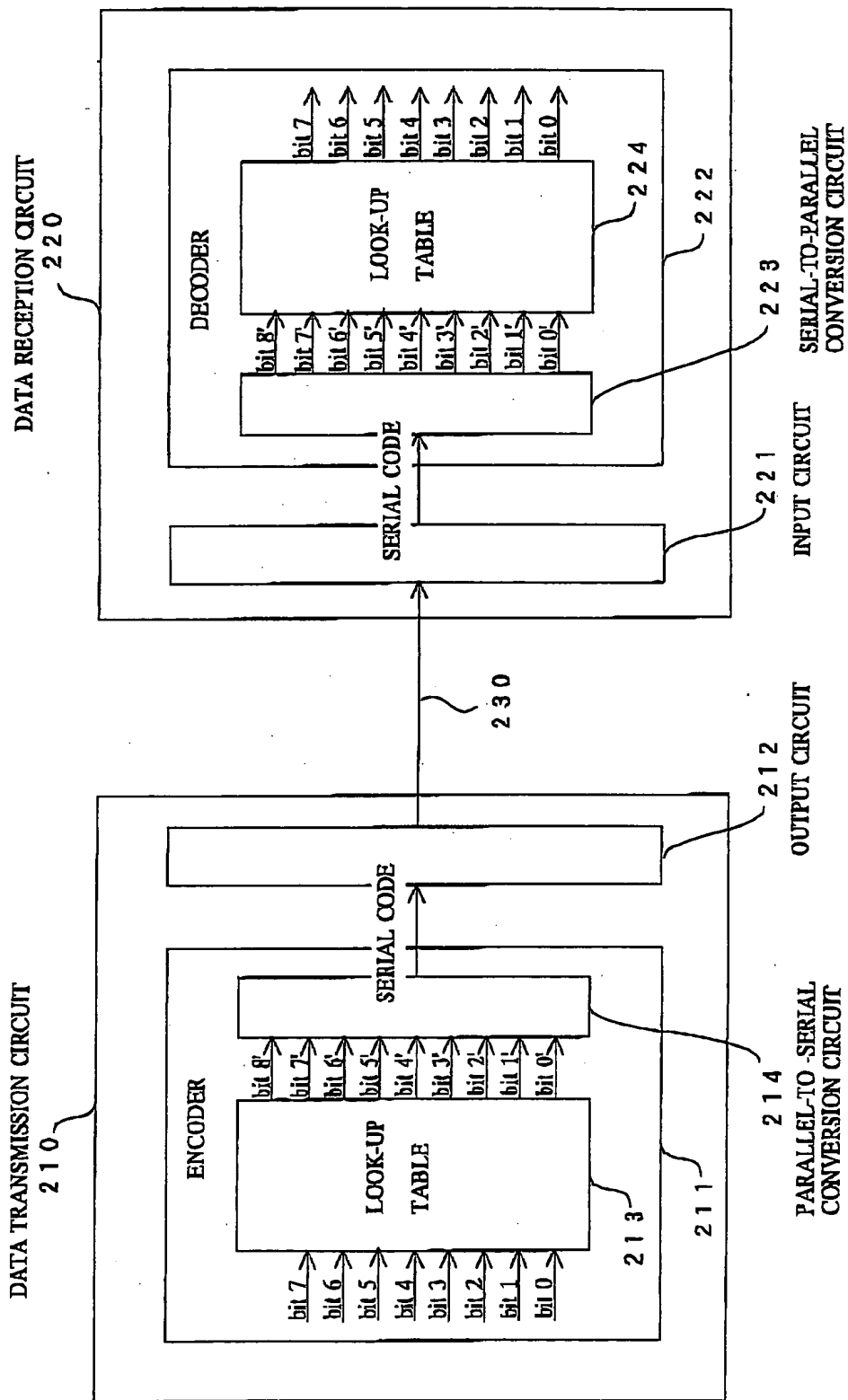


FIG. 2

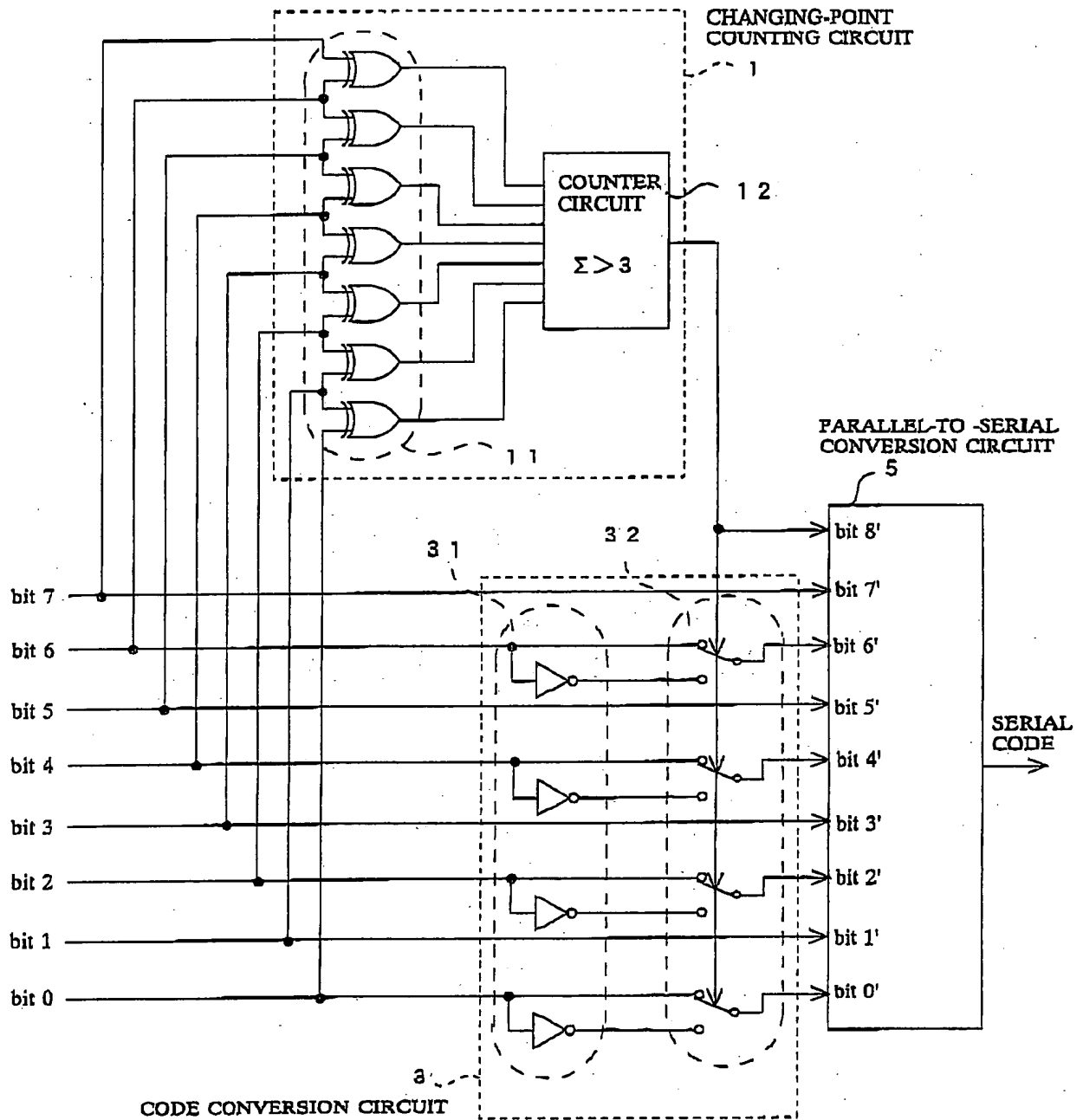


FIG. 3A

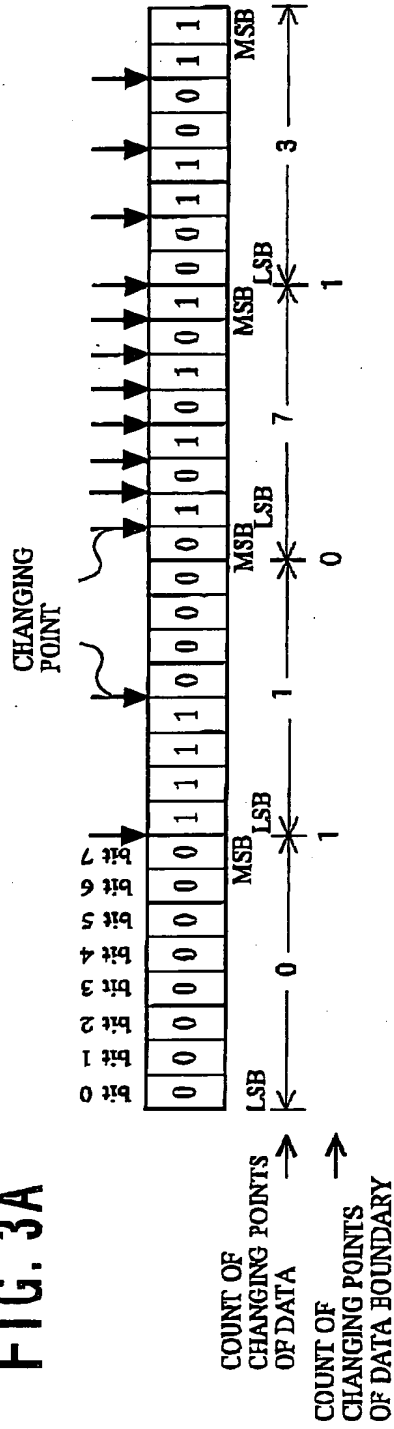


FIG. 3B

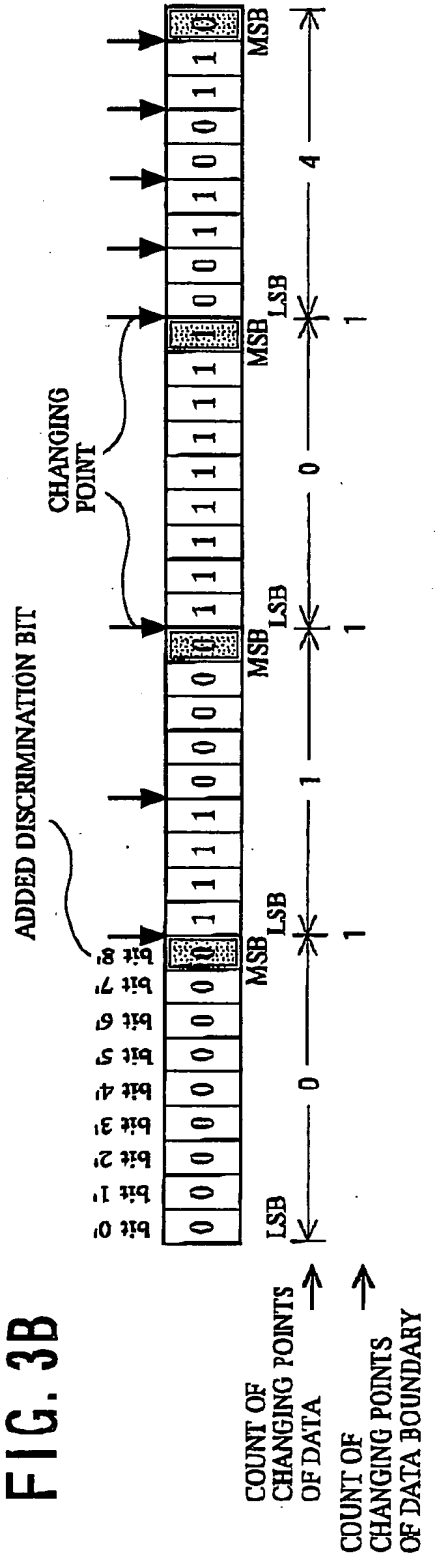


FIG. 4A

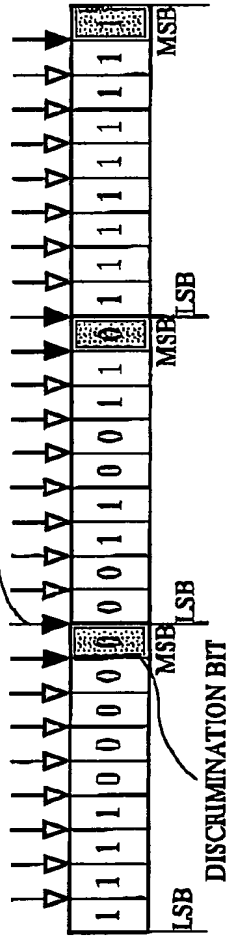
DISCRIMINATION BIT IS PLACED AT MSB

↓ : UNCONTROLLABLE CHANGING POINT (PROBABILITY: 50%)

↓ : CONTROLLABLE POINT (PROBABILITY: LESS THAN 50%)

FRONT AND REAR EDGES
OF DISCRIMINATION BIT

DATA BOUNDARY



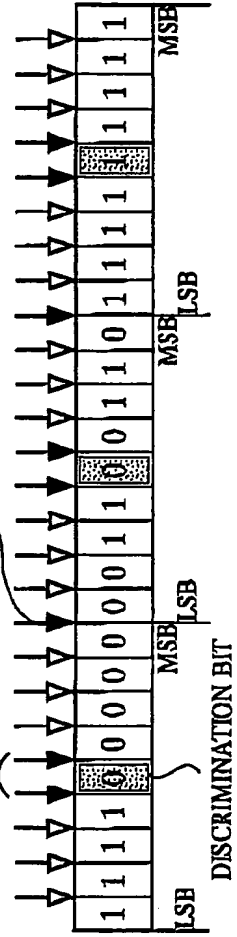
<UNCONTROLLABLE CHANGING POINTS ARE LOCATED BEFORE AND AFTER
DISCRIMINATION BIT AND AT DATA BOUNDARIES.>

FIG. 4B

DISCRIMINATION BIT IS PLACED AT OTHER THAN MSB AND LSB

FRONT AND REAR EDGES
OF DISCRIMINATION BIT

DATA BOUNDARY



<UNCONTROLLABLE CHANGING POINTS INCREASE.>

FIG. 5

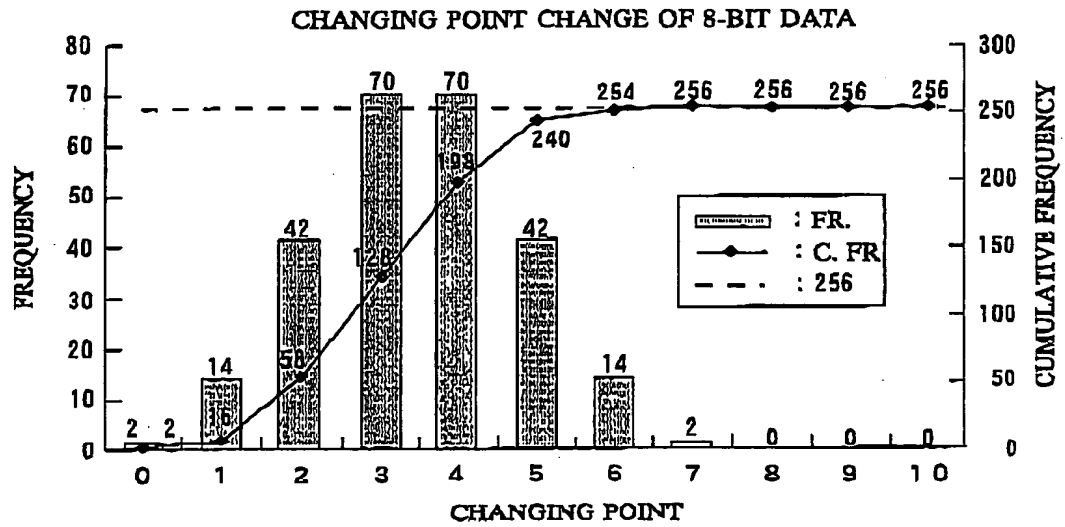


FIG. 6

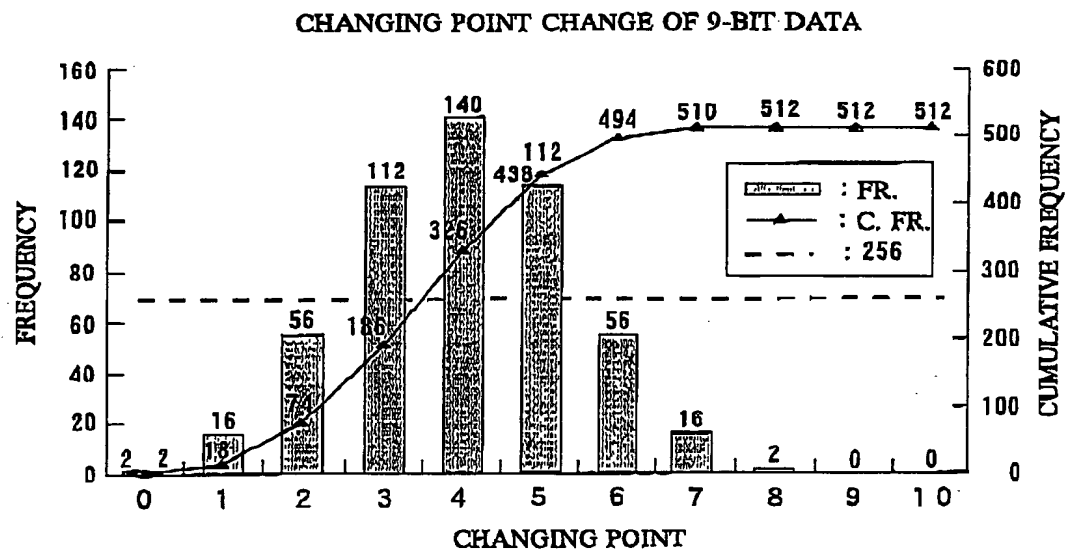


FIG. 7

CHANGING POINT CHANGE AFTER CONVERSION TO 9-BIT DATA

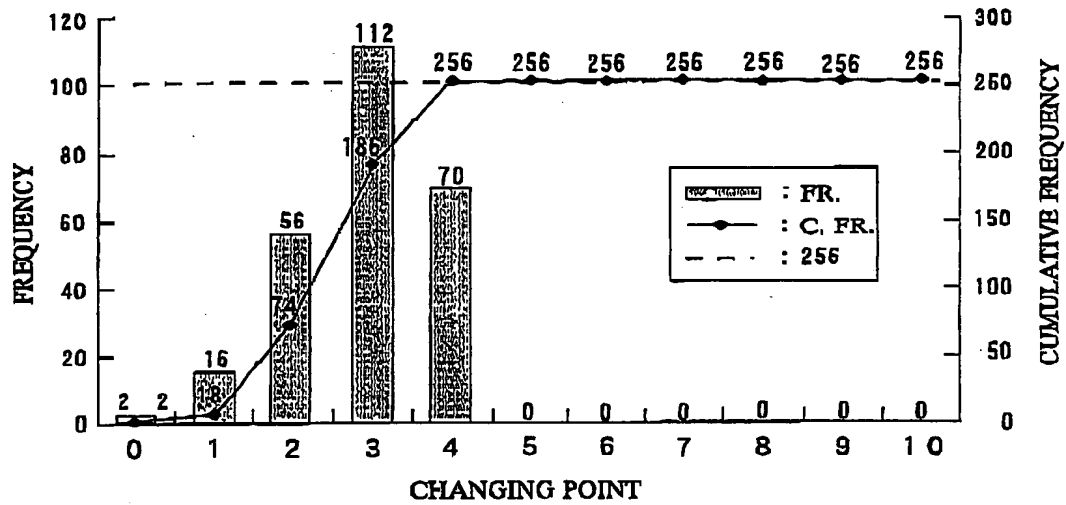


FIG. 8

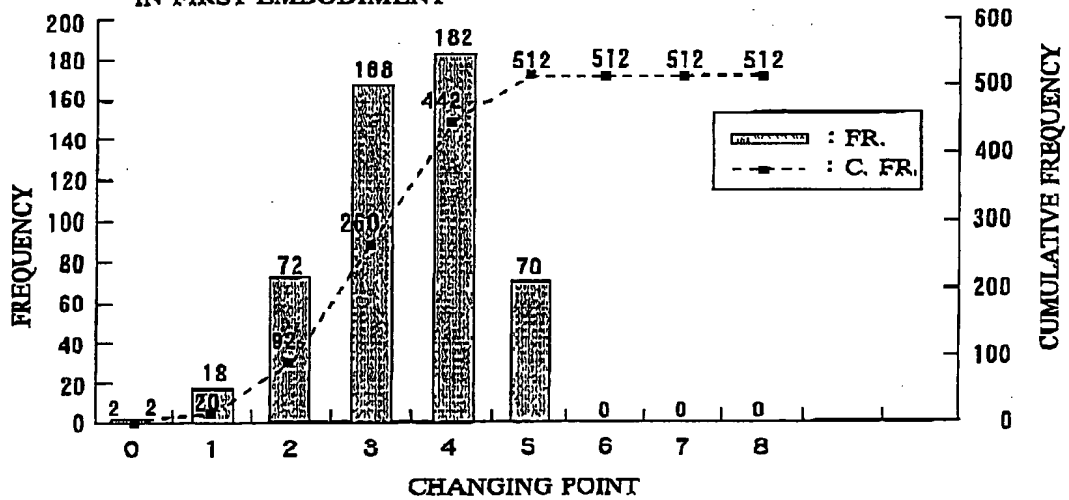
CHANGING POINT CHANGE INCLUDING DATA BOUNDARIES
IN FIRST EMBODIMENT

FIG. 9

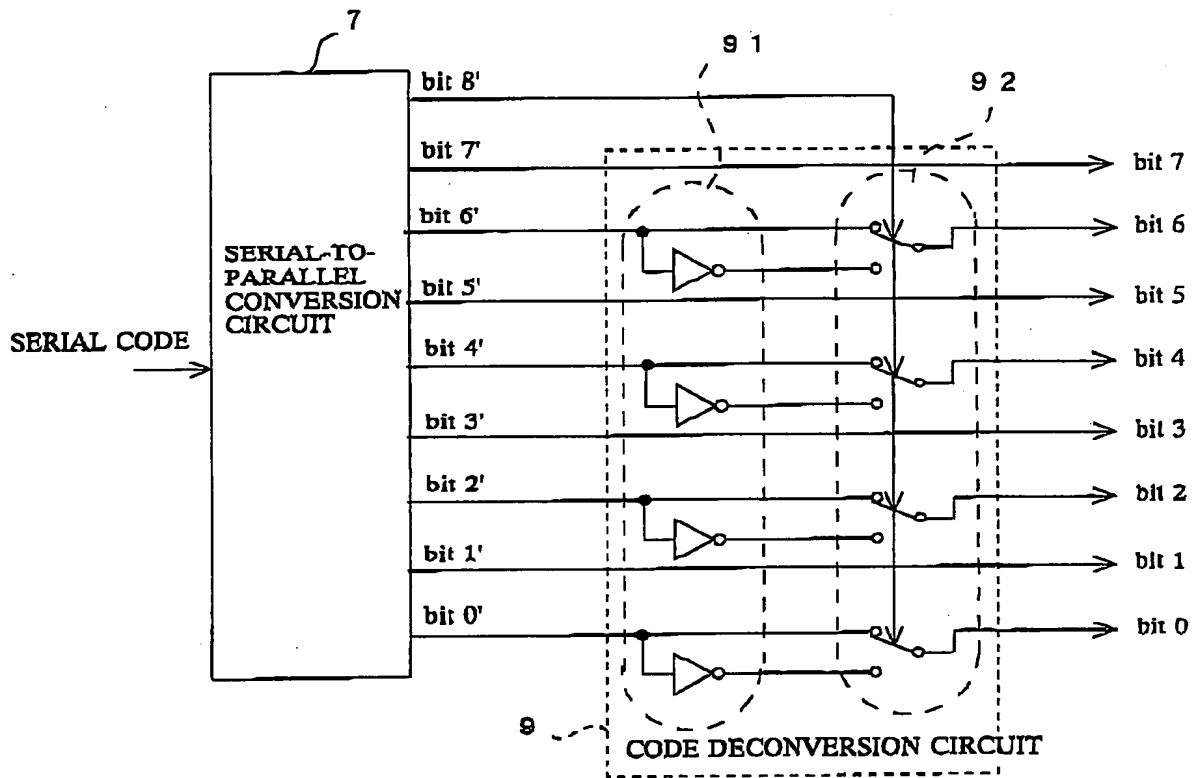


FIG. 10

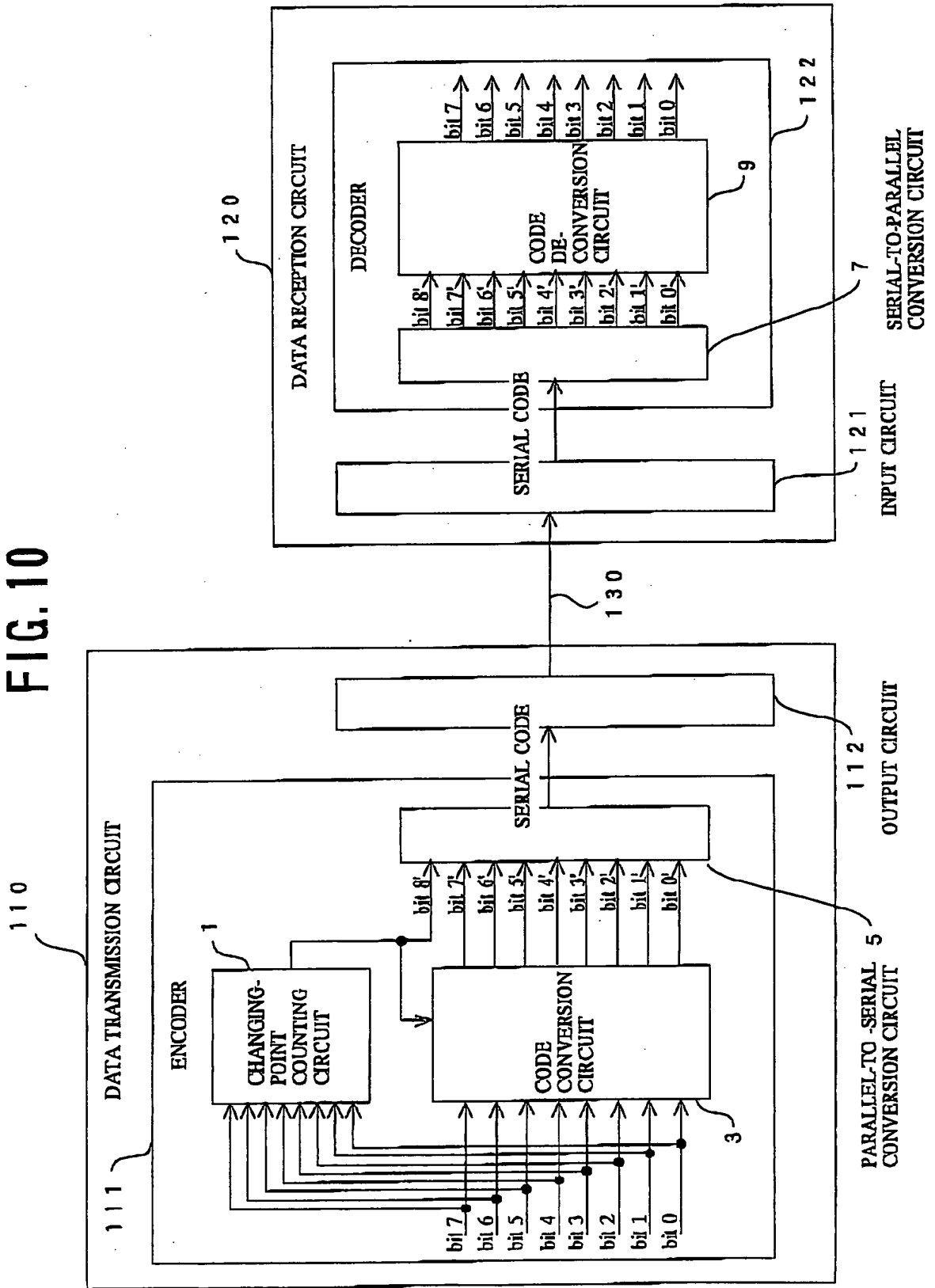


FIG. 11

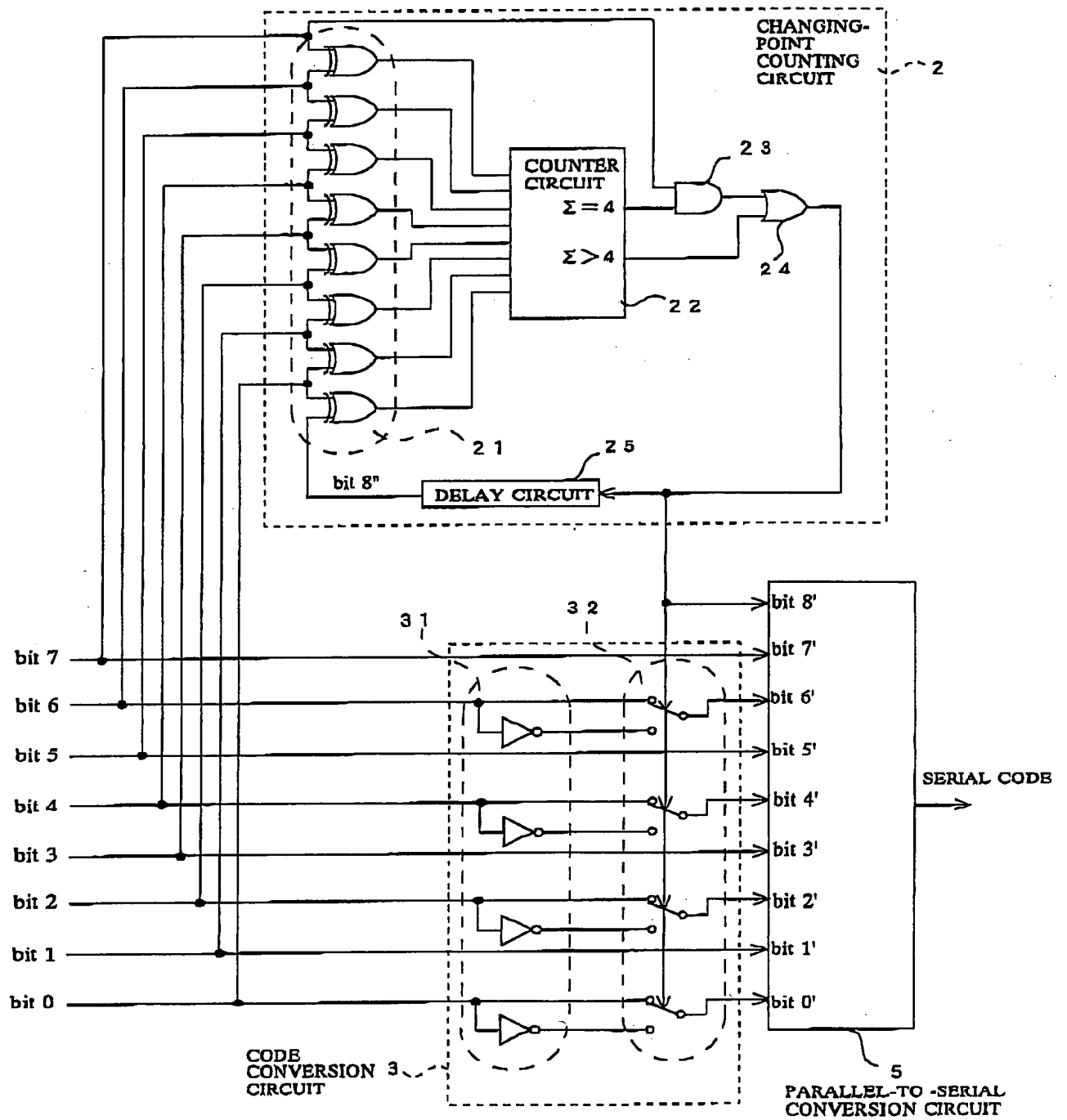


FIG. 12A

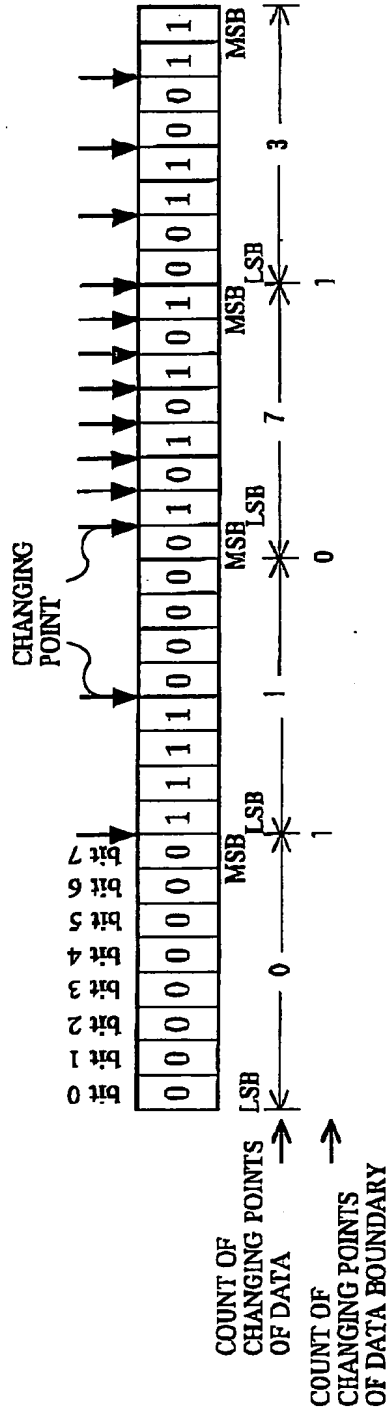


FIG. 12B

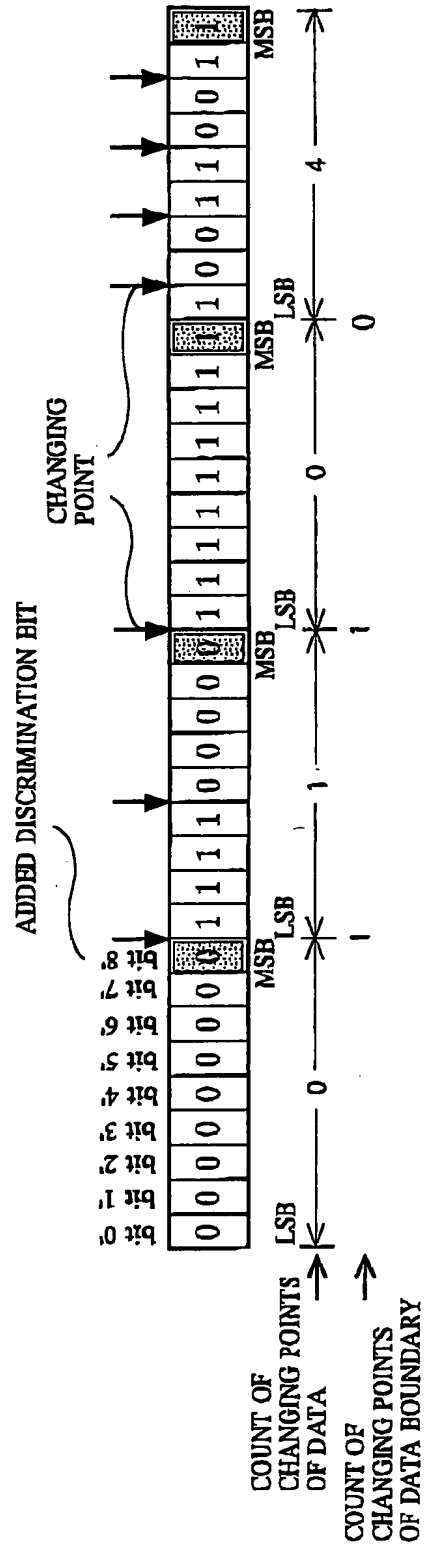


FIG. 13

CHANGING POINT CHANGE INCLUDING DATA BOUNDARIES
IN FOURTH EMBODIMENT

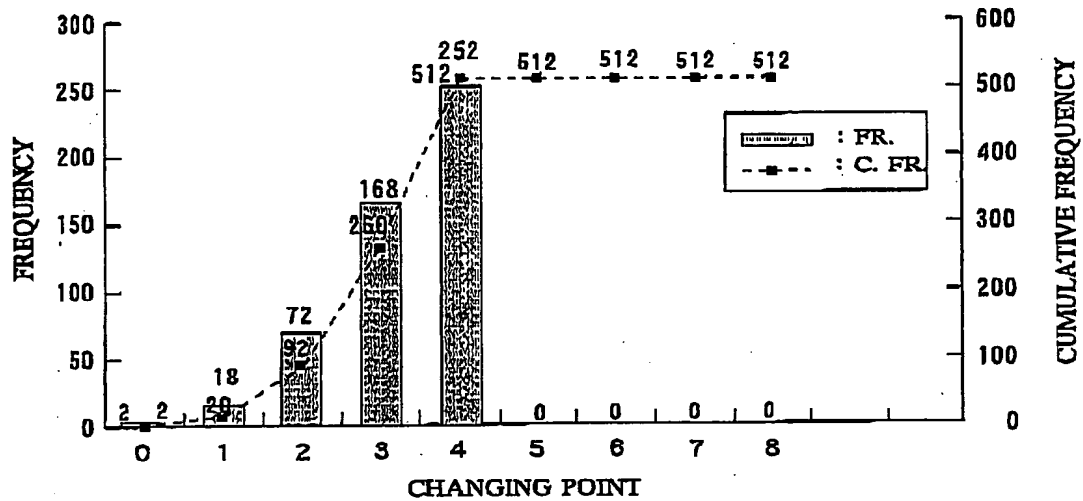


FIG. 14

CHANGING POINT CHANGE OF 10-BIT DATA

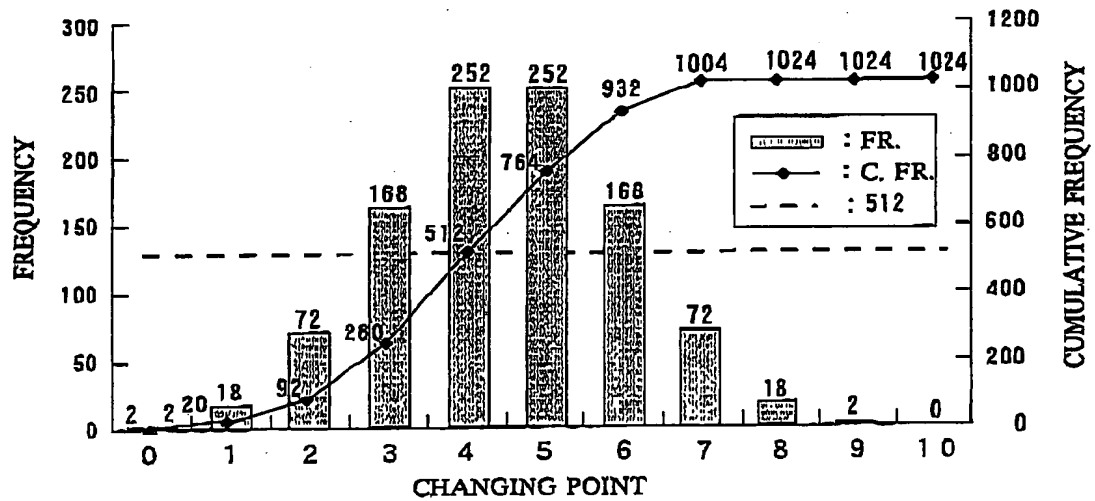


FIG. 15

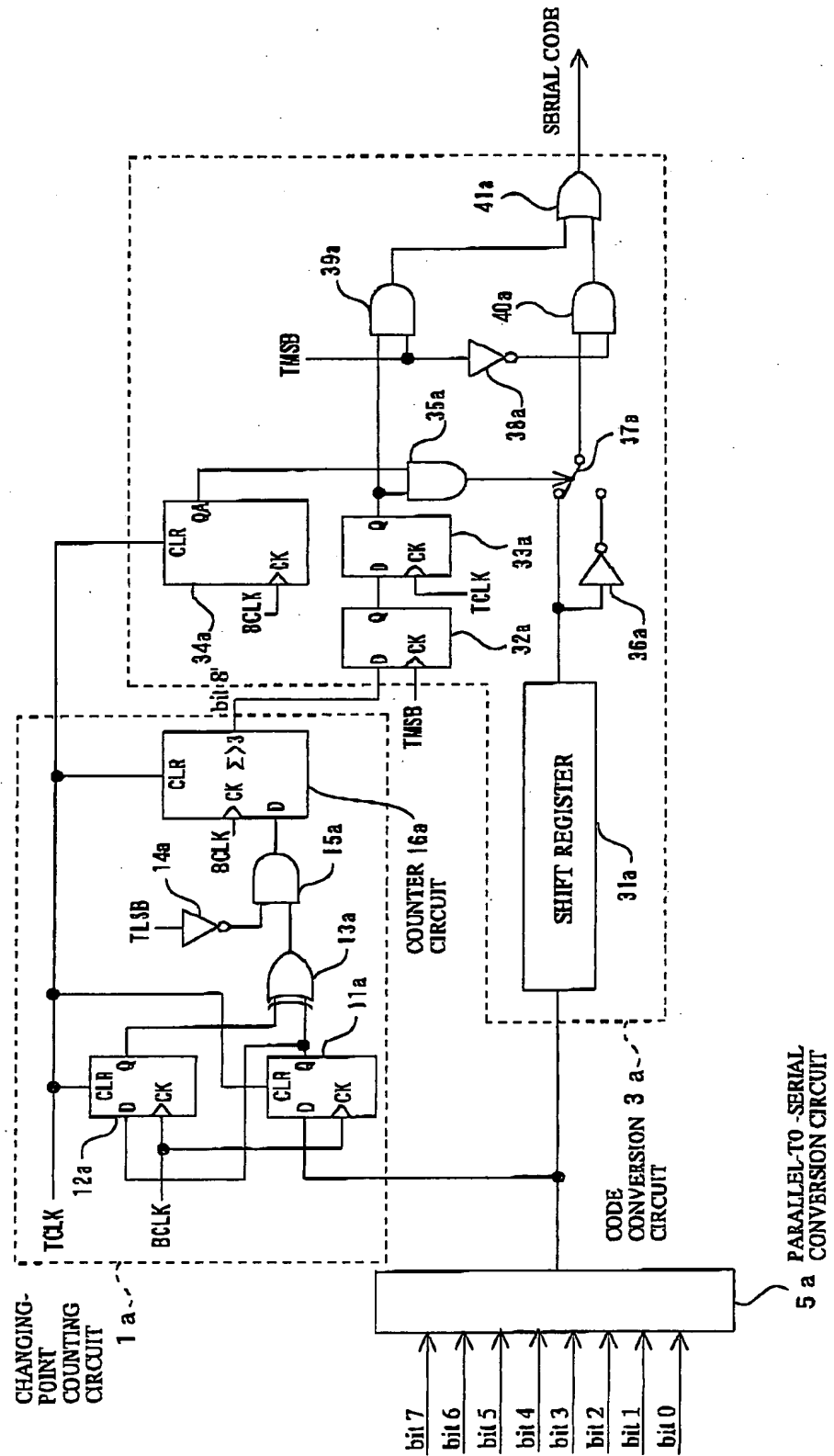


FIG. 16

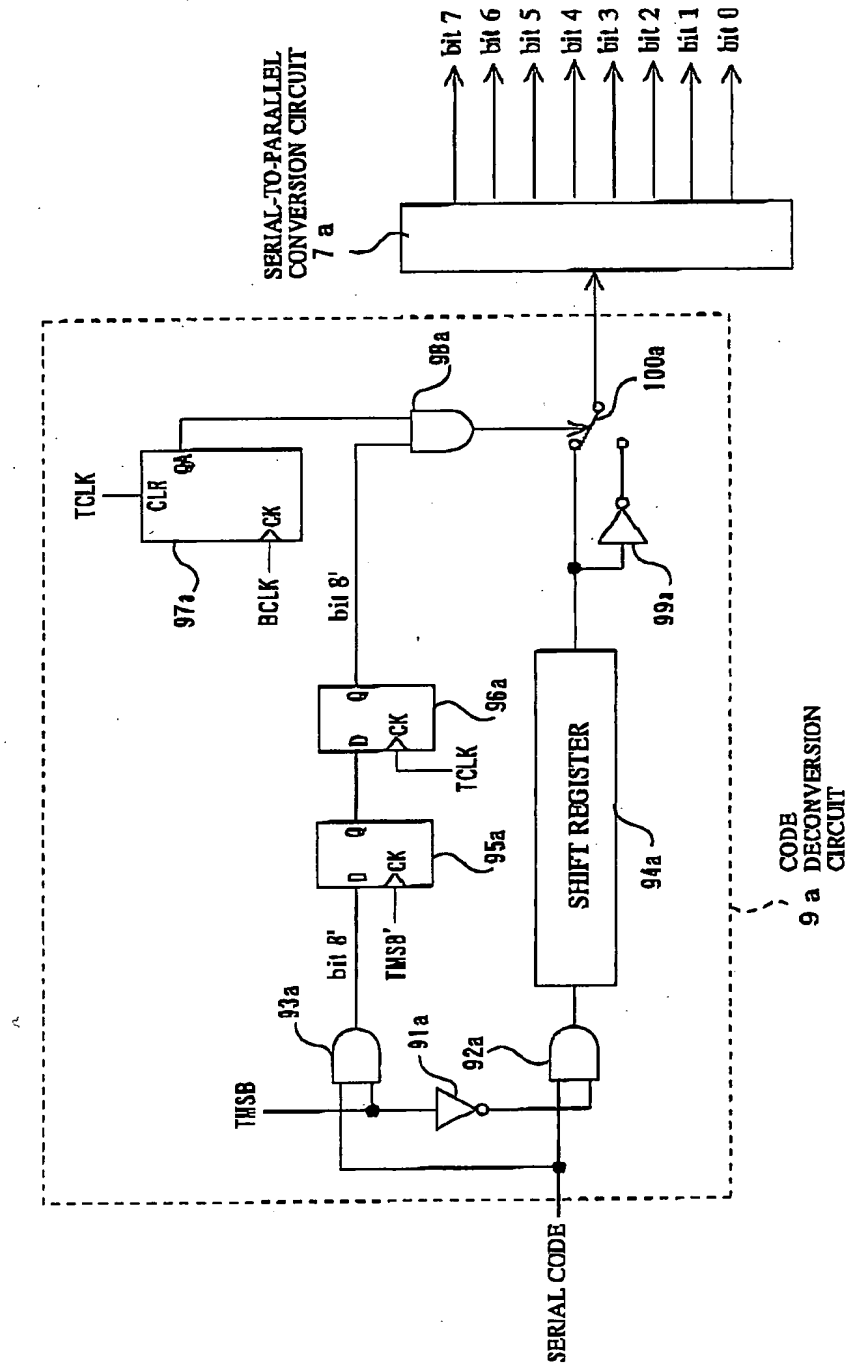
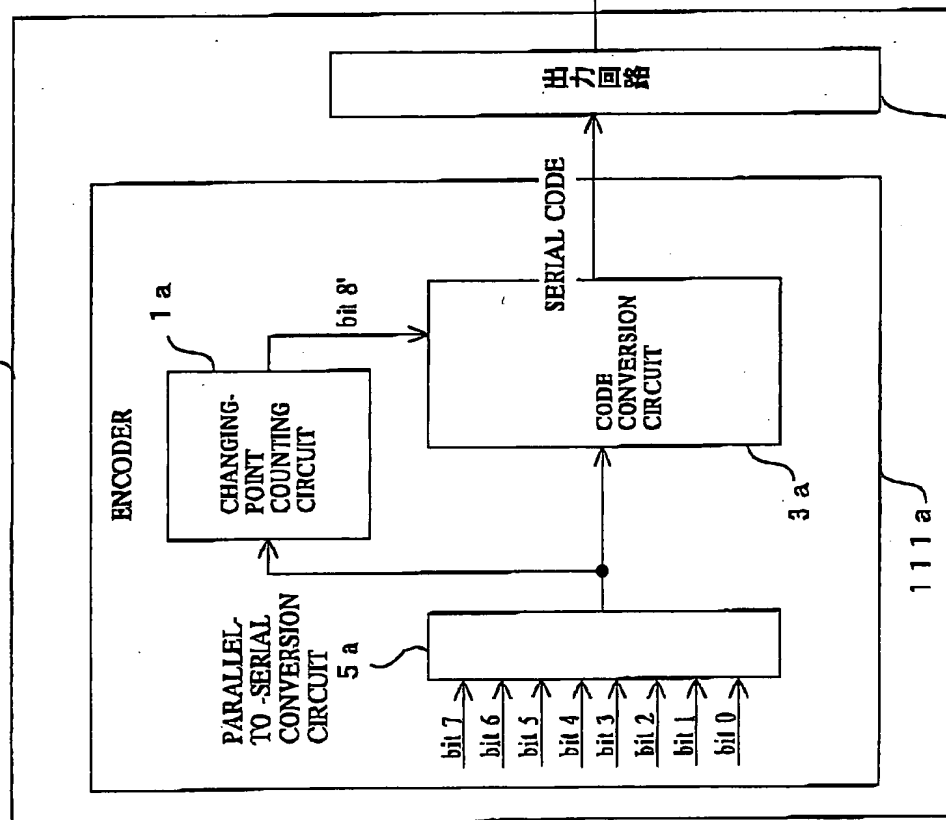


FIG. 17

DATA TRANSMISSION CIRCUIT

110 a



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DATA RECEPTION CIRCUIT

120 a

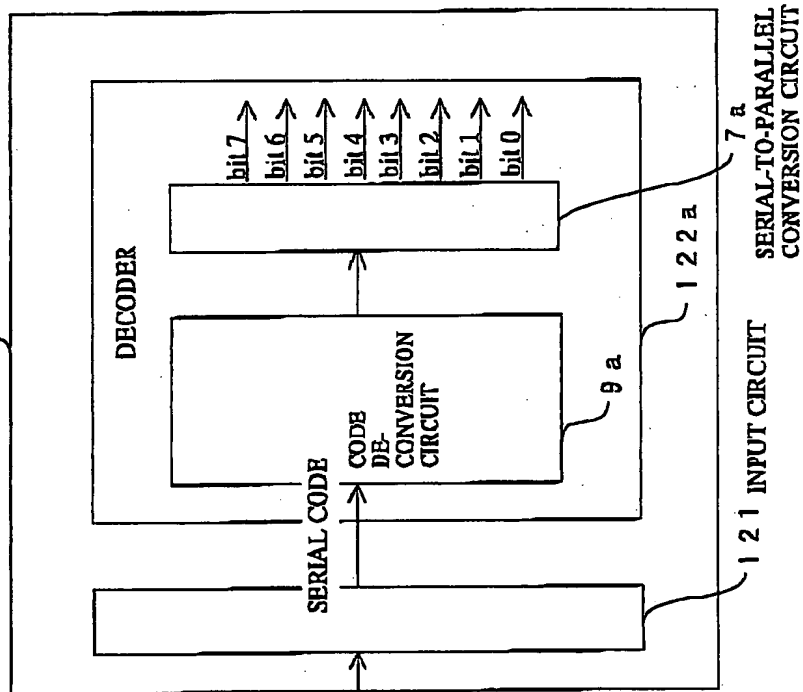


FIG. 18

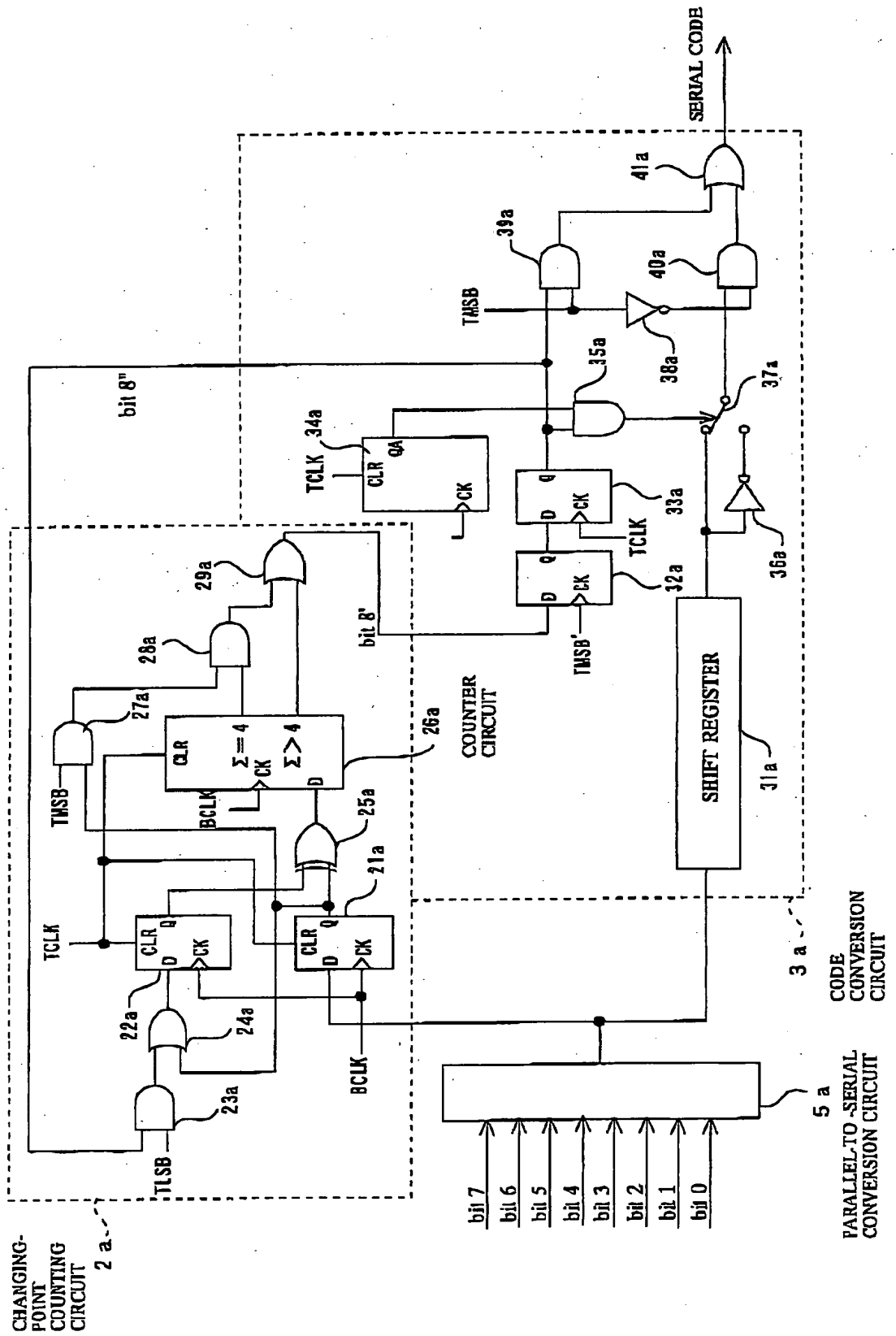


FIG. 19

